Amendments to the Claims

Please amend claims 1-4, 7-13 & 16-18 and add new claims 23 & 24 as set forth below. In accordance with the revised amendment practice, changes in the amended claims are shown by underlining (for added matter) and strikethrough (for deleted matter).

---

1. (Currently Amended) A method for controlling power of a computer in which at least a power-on self test for hardware is carried out before shifting into an operating system process, when the power is turned on, comprising:

obtaining ~~reading out~~ a result of said power-on self test; and

automatically turning on the power again a defined period of time after stopping the power supply to said computer when a predetermined test result of said power-on self test has been obtained ~~read out~~.

2. (Currently Amended) The method according to Claim 1, wherein said predetermined test result includes a test result indicating that a ~~any~~ hardware component contained in said computer is not initialized correctly.

3. (Currently Amended) The method according to Claim 1, wherein ~~the~~ operation of a ~~any~~ hardware component contained in said computer is stabilized when said predetermined test result has been obtained ~~read out~~.

4. (Currently Amended) The method according to Claim 2, wherein ~~the~~ operation of a ~~any~~ hardware component contained in said computer is stabilized when said predetermined test result has been obtained ~~read out~~.

5. (Original) The method according to Claim 3, wherein at least one selected from the group consisting of a setup for stabilizing the operation of a power circuit in said computer, a setup for cooling the inside of said computer, and a setup for disabling a function to suppress power consumption is carried out to stabilize the operation of said hardware component.

6. (Original) The method according to Claim 4, wherein at least one selected from the group consisting of a setup for stabilizing the operation of a power circuit in said computer, a setup for cooling the inside of said computer, and a setup for disabling a function to suppress power consumption is carried out to stabilize the operation of said hardware component.

7. (Currently Amended) The method according to Claim 1, wherein the power is inhibited to be turned on again after the power supply to said computer is stopped when said predetermined test result has been obtained ~~read out at~~ a predetermined number of times.

8. (Currently Amended) The method according to Claim 5, wherein the power is inhibited to be turned on again after the power supply to said computer is stopped when said predetermined test result has been obtained ~~read out at~~ a predetermined number of times.

9. (Currently Amended) The method according to Claim 6, wherein the power is inhibited to be turned on again after the power supply to said computer is stopped when said predetermined test result has been obtained ~~read out at~~ a predetermined number of times.

10. (Currently Amended) A power control apparatus for controlling power of a computer in which at least a power-on self test for hardware is carried out before shifting into an operating system process, when the power is turned on, comprising:

a holding ~~readout~~ unit for obtaining ~~reading out~~ a result of said power-on self test; and

a control unit for automatically controlling ~~to~~ turn on of ~~the~~ power again a defined period of time after stopping the power supply to said computer when a predetermined test result of said power-on self test has been obtained ~~read out~~ by said holding ~~readout~~ unit.

11. (Currently Amended) The power control apparatus according to Claim 10, wherein said predetermined test result includes a test result indicating that a ~~any~~ hardware component contained in said computer is not initialized correctly.

12. (Currently Amended) The power control apparatus according to Claim 10, wherein said control unit controls to carry out a setup for stabilizing ~~the~~ operation of a ~~any~~ hardware component contained in said computer when said predetermined test result has been obtained ~~read out~~.

13. (Currently Amended) The power control apparatus according to Claim 11, wherein said control unit controls to carry out a setup for stabilizing ~~the~~ operation of a ~~any~~ hardware component contained in said computer when said predetermined test result has been obtained ~~read out~~.

14. (Original) The power control apparatus according to Claim 12, wherein said setup for stabilizing the operation of said hardware component is at least one selected from the group consisting of a setup for stabilizing the operation of a power circuit in said computer, a setup for cooling the inside of said computer, and a setup for disabling a function to suppress power consumption.

15. (Original) The power control apparatus according to Claim 13, wherein said setup for stabilizing the operation of said hardware component is at least one selected from the group consisting of a setup for stabilizing the operation of a power circuit in said computer, a setup for cooling the inside of said computer, and a setup for disabling a function to suppress power consumption.

16. (Currently Amended) The power control apparatus according to Claim 10, wherein said control unit inhibits the power to be turned on again after the power supply to said computer is stopped when said predetermined test result has been obtained ~~read out at~~ a predetermined number of times.

17. (Currently Amended) The power control apparatus according to Claim 14, wherein said control unit inhibits the power to be turned on again after the power supply to said computer is stopped when said predetermined test result has been obtained ~~read out at~~ a predetermined number of times.

18. (Currently Amended) The power control apparatus according to Claim 15, wherein said control unit inhibits the power to be turned on again after the power supply to

said computer is stopped when said predetermined test result has been obtained ~~read out at~~ a predetermined number of times.

19. (Original) A computer, comprising:

a power control apparatus for controlling power of said computer according to Claim 10;

a power unit being controlled by said power control apparatus; and

a computer load operating on the power supplied by said power unit.

A1

20. (Original) A computer, comprising:

a power control apparatus for controlling power of said computer according to Claim 16;

a power unit being controlled by said power control apparatus; and

a computer load operating on the power supplied by said power unit.

21. (Original) A computer, comprising:

a power control apparatus for controlling power of said computer according to Claim 17;

a power unit being controlled by said power control apparatus; and

a computer load operating on the power supplied by said power unit.

22. (Original) A computer, comprising:

a power control apparatus for controlling power of said computer according to Claim 18;

a power unit being controlled by said power control apparatus; and

a computer load operating on the power supplied by said power unit.

23. (New) The method according to claim 1, wherein said automatically turning on occurs without making an operator of the computer aware of said result of said power-on self test.

24. (New) The power control apparatus according to claim 10, wherein the automatically controlling turn on of power occurs without making an operator of the computer aware of said result of said power-on self test.

* * * * *